

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon SIM, et al.

Serial No. 09/901,930 Examiner: Wells, Kenneth

Filed: July 9, 2001 Art Unit: 2816

Confirmation No. 7100

For: NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR
MEMORY DEVICE

Date: December 27, 2007

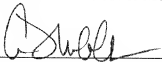
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**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance, the Examiner recited a portion of an allowed independent claim. The applicants note that it is a well-known tenet of patent law that each allowed patent claim should be considered as a whole. The dependent claims also define patentable subject matter. Further, although the Examiner has indicated one reason for allowance, there can be other reasons that claims are allowable.

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Respectfully submitted,
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